PATENTAC

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inte application of: Hon Wah Chin

Application No.: 0

plication No.: 09/164,388

Filed: September 30, 1998

Title: REDUCING CPU OVERHEAD IN THE

FORWARDING PROCESS

Attorney Docket No.: CISCP054

Examiner: Prieto, B.

JAN 1 0 2002

Group: 2152

Technology Center 2100

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail to: Assistant Commissioner for Patents, Washington, DC 20231 on October 26, 2001.

Signed:

Deborah Neill

PRELIMINARY AMENDMENT

Assistant Commissioner for Patent Washington, D. C. 20231

Sir:

The following remarks are provided in response to the Office Action dated June 27, 2001. Both a marked-up copy of the claims and a clean replacement copy of the claims are provided for the convenience of the Examiner.

In the **CLAIMS**:

Please ADD new claims as follows:

45. (New) The method as recited in claim 2, further comprising:

when the interrupt is asserted, transferring the one of the plurality of inbound queues to the outbound queue or an outbound controller associated with the outbound queue.